

PATENT

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re patent application of:

Applicant(s): Jagrut Viliskumar Patel

Examiner: Aditya S. Bhat

Serial No: 10/750,342

Art Unit: 2863

Filing Date: December 31, 2003

Conf. No: 9469

Title: IDENTIFYING PROCESS AND TEMPERATURE OF SILICON CHIPS

**Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450**

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**SUPPLEMENTAL AMENDMENT**

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Dear Commissioner:

This paper is submitted as a supplement to the response filed August 7, 2008 to the Non-Final Rejection dated April 8, 2008. Please enter the following amendments and consider the following remarks.

**Amendments to the Specification** begin on page 2 of this paper.

**Remarks** begin on page 3.

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**CERTIFICATE OF TRANSMISSION (37 CFR 1.8(a))  
ELECTRONIC FILING**

I hereby certify that this correspondence is being transmitted electronically to the United States Patent and Trademark Office on November 20, 2008.

Depositor's Name: **Gayle Gestick**  
(Typed name)

Signature: /Gayle Gestick/ Date: November 20, 2008